


INDIVIDUAL FACULTY PROFILE- TEMPLATE TO BE UPLOADED IN COLLEGE WEBSITE

	<p>Name in Caps : ARCHANA H R Designation: ASSISTANT PROFESSOR Qualification :BE,MTech,(PhD) Email-id: Experience: Teaching experience: Date of Joining this Institution (BMSCE):</p> <p><u>ResearchInterests: VLSI, System on Chip, Network on Chip</u></p>
<p>About Your self</p>	<p>Paragraph (Minimum 10 Lines) –</p> <p>I have completed my BE , MTech in Electronics from VTU and pursuing PhD in VLSI, SoC. I have 10 Years of Teaching experience and 1 year industry experience .I have presented and published papers in 16 national and international conferences and journals of which one paper is a scopus indexed and yet another being published in Springer journal series. I have guided 4 projects at Masters level. I have interacted with people from different Semiconductor industries like Waferspace Technologies, Sevitech Systems Pvt Ltd, ON Semiconductors and Analog Micro Circuits to facilitate internship for Mtech students and attended IEEE Blended Learning Programme.</p>
	<p><u>Education:</u> Doctoral Research: Pursuing Masters: Mtech(Electronics) Bachelors: BE(ECE) Other Degree/Diploma/PG diploma if any</p>
	<p>Personal web site/page if any then mention the Webpage link</p>
	<p>Selected Publications 1)An Investigation towards Effectiveness in Image Enhancement Process in MPSoC 2) A Novel Design and Implementation of Imaging Chip Using AXI Protocol for MPSoC on FPGA</p>
	<p>Courses Handled/List Analog Microelectronics Mixed Signal Design System On Chip Elements of Electronics Engg Analog Integrated circuits Electronic Instrumentation</p>

	Additional Responsibilities:
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Proctoring

Co ordination for valuation

Co ordination for Conferences and workshop

Co ordinator for Graduation day and Orientation programs