

INDIVIDUAL FACULTY PROFILE- TEMPLATE TO BE UPLOADED IN COLLEGE WEBSITE

Name in Caps : **PRIYADARSHINI JAINAPUR**
Designation : **Assistant Professor**
Qualification : **B.E, M.Tech.**
Email-id: (official): priyadarshinjainapur.ece@bmsce.ac.in
Experience Total : **11 years**
Teaching experience : **11 years**
Date of Joining this Institution (BMSCE): 12/06/2023

Research Interests:

VLSI Design and Embedded Systems, Verilog HDL, Mixed Signal Design, Real Time Systems.

About Your self	I have over 11 years of professional experience, with a strong specialization in VLSI Design. For the past 9 years, I have been serving as an Assistant Professor at BMS Evening College of Engineering, Bangalore, where I have been actively involved in teaching, mentoring, and guiding students in both academic and practical aspects. I am committed to contributing meaningfully to the student community by continuously improving my teaching methodologies and staying updated with advancements in VLSI and related technologies.
Education:	<p>Masters: M.Tech (VLSI Design and Embedded System) NMIVIU, Belgaum, Karnataka</p> <p>Bachelors: B.E. (Telecommunication Engineering) From BMSIT V.T.U., Belgaum, Karnataka</p>
Journals Published	<ol style="list-style-type: none"> 1. "Modeling and Design of a CMOS Low Drop-out (LDO) Voltage Regulator" - International Journal of VLSI and Embedded Systems- IJVES (ISSN: 2249 – 6556) 20-June-2013 2. " A model for automated detection and control of air pollution from vehicles" -Int. J. Engg. Sc. & Mgmt. Vol. VII Issue II Jul - Dec 2016 3. "A model for Face recognition system using voila-jones Algorithm," - Int. J. Engg. Sc. & Mgmt. Vol. VII Issue II Jul - Dec 2017 4. "Intelligent Ambient Light-Controlled Street Lighting System for Power Optimization", - International Journal of Innovative Research in Electrical, Electronics, Instrumentation and Control Engineering, Vol. 12, Issue 5, May 2024(ISSN (O) 2321-2004, ISSN (P) 2321-5526) 5. "Automation of Integrated Circuit Parameters Measurement using LabVIEW"- -International Journal of Innovative Research in Electrical, Electronics, Instrumentation and Control Engineering, Vol 12, Issue 7, July 2024(ISSN (O) 2321-2004, ISSN (P) 2321-5526) 6. "HEART ATTACK, ALCOHOL DETECTION AND MONITORING IN SMART TRANSPORTATION SYSTEM"-International Journal of Innovative Research in Electrical, Electronics, Instrumentation and Control Engineering, Vol 12, Issue 7, July 2024(ISSN (O) 2321-2004, ISSN (P) 2321-5526) 7. "Enhancing Real-Time Efficiency in Metro Ticketing Systems"-Journal of Emerging Technologies and Innovative Research, Volume 11 Issue 9 September 2024 (ISSN:2349-5162) 8. "Embedded Technology for Soldier Health and Safety Monitoring", Journal of VLSI Design and Signal Processing,e-ISSN: 2581-8449, Vol. 10, Issue 3 (September – December, 2024) pp: (1-9). 9. "AI-Assisted Classification of Rice Diseases and Insect Pests for Crop Growth Management", Journal of Information Systems Engineering and Management, Vol. 10 No. 47s (2025) pp.(409-414).

	<p>10. "Deep Reinforcement Learning-Based Energy-Aware Intrusion Prevention in IoT Environment", International Journal of Safety and Security Engineering, Vol. 15, No. 8, August, 2025, pp. 1745-1754.</p> <p>11. Resource optimization of approximate convolutional neural network-based accelerator on FPGA platform", Bulletin of Electrical Engineering and Informatics, Vol. 15, No. 2, April 2026, pp. 1049~1060, ISSN: 2302-9285.</p>
<p>Conferences / workshops attended</p>	<ol style="list-style-type: none"> 1. Participated in ISTE- BMSCE Chapter one week faculty development programme on "Outcome Based Education" between 16 /8/2022 to 20/08/2022 organized by B.M.S. Evening college of Engineering. 2. Participated and Successfully completed One Week AICTE – VTU Joint Teachers Training Programme on "An Overview of Teaching Techniques in Data Science and Analytics" between 25th & 29th April 2022 Organized by VTU Human Resource Development Centre (VTU - HRDC), Centre for PG Studies, VIAT. 3. Participated and successfully completed One Week Teacher Training Programme AICTE – VTU on "Introduction to Python Programming & its Applications" 13th -17th March 2023. 4. Participated and Successfully completed One Week Teacher Training Programme AICTE – VTU on "Analog VLSI Design using EDA Tools" 20th -25th March 2023. 5. Participated and successfully completed One Week programme on "Research Methodology - Tools and Techniques" 5th Sept – 9th Sept 2022. 6. Participated in one week National level FDP on "Artificial Intelligence and Machine Learning" conducted by A.C.T Academy from 04.12.2023 – 11.12.2023. 7. Attended FDP on "Emerging technologies" organized by Department of CSE, Vidyavardhaka College of Engineering in association with TCS held from 11.12.2023 – 15.12.2023 8. Participated in two weeks FDP on "Recent trends in technology" held during 23.06.2025 – 04.07.2025. 9. Completed FDP on "AI Mastery for Educators" held during 15.05.2025-07.06.2025. 10. Participated in FDP on "Smart Sensors, IOT and AI in agriculture: innovations for sustainable farming" during 19.05.2025 – 24.05.2025. 11. Participated in FDP on "Artificial Intelligence and Machine Learning" during 16.05.2024 – 31.05.2024. 12. Participated in 5 days online FDP on " Emerging Technologies – Shaping the future during 12.05.2025 – 16.05.2025. 13. Participated in two weeks FDP on " Trainer training for developing employability" from 3rd Sept to 12th Sept 2025. 14. Participated in one week FDP on " Empowering Educators: Leadership, Research and Pedagogical experience" from 17.02.2025 – 22.02.2025.

Courses Handled	Verilog HDL, Fundamental of VLSI, VLSI Design, Verilog HDL, Computer Communication Networks, Embedded Systems, Real Time Operating Systems, Analog Integrated Circuits, Mixed Signal Design.
Additional Responsibilities	<ol style="list-style-type: none">1. Department Placement Officer2. Department Internship Coordinator

