



Name **CHAITANYA .L**
Designation **Assistant Professor**
Qualification **B.E., M.Tech**
Email-id: (official) **chaitanya.eee@bmsce.ac.in**
Total Experience: **8 years**
Teaching experience: **8 years**
Date of Joining this
Institution (BMSCE): **21/8/2014**

Research Interests:

Power Electronics

About Myself

I am associated with department of Electrical and Electronics, BMS College of Engineering since 2014. I have done Master's Degree in Power Electronics from VTU. I have few research papers including Educational paper. My areas of interest include Power Electronics, Multilevel converter topologies. Other than teaching responsibility, I have served as faculty coordinator for department level workshops, STTPs and invited lectures. I am also the faculty coordinator for PRAKRUTHI – The Nature club of BMSCE.

Education:

Doctoral Research:

Masters: **M.Tech in Power Electronics, VTU**

Bachelors: **B.E in Electrical and Electronics, VTU**

PG diploma if any : Post Graduation Diploma in Industrial Automation (PGDIA) by Technocrat Automation Solution Pvt. Ltd.

Personal web site/page if any then mention the Webpage link

<https://scholar.google.co.in/citations?user=76N6h2sAAAAJ&hl=en>

Selected Publications

1. **“Sub Module Voltage Balancing In Modular Multilevel Converter** “in IEEE- International Conference on Circuits, Communication, Control and computing - (I4C 2014) organized by MSRIT, Bangalore.
2. **“ Impact Of Assessment Techniques On Learning Outcomes – A Case Study ”**in the Fifth International Conference on **Transformations** in Engineering Education (ICTIEE) 2018 organized by Thiagarajar College of Engineering Madurai in association with Indo-Universal Collaboration for Engineering Education (IUCEE).
3. **“2d And 3d Plotter Using Arduino Nano”** in International Journal of Engineering Applied Sciences and Technology, Santhosh M P, Jayanth H S, Kirankumar S Patil, Jagadeesh Kumar Chougala, Chaitanya.L, in International Journal of Engineering Applied Sciences and Technology, 2021 Vol. 6, Issue 1, ISSN No. 2455-2143, Pages 331-334.

4. **“Synthetic Battery Attribute Generation To Surmount Data Scarcity Using Auto-Correlation Mechanism** “Channegowda, Janamejaya, Vinayak Raj Urs, and Chaitanya Lingaraj. International Journal of Energy Research.

5. **“RGB Camera Based Object Detection And Object Coordinate Extraction** “ with UG students Apoorva M, Nikhil Muralidhar Shanbhogue, Samarth Shreepad Hegde Yogesh P Rao, Chaitanya.L in 2022 IEEE 7th International conference for Convergence in Technology (I2CT) Pune, India. Apr 07-09, 2022

6. **Implementation Of Open-Ended Lab Assessment For Enhanced Learning Experience**’ Prema V, Soniya Agrawal, Divya S and Chaitanya L , IUCSEE Mini Symposium on Effective Practices in Teaching-Learning on 23rd – 24th April 2022.

7. **An Attention-Based Synthetic Battery Data Augmentation Technique To Overcome Limited Dataset Challenges**, Janamejaya Channegowda, Vageesh Maiya, Niharika Joshi, Vinayak Raj Urs, Chaitanya Lingaraj , Energy Storage. 2022;e10315. doi:10.1002/est2.354. Paper is Indexed in Web of Science - ISSN:2578-4862.

8. **“Attention –based Battery Parameter Augmentation Technique to Alleviate Limited Data Challenges** “at the 8th International Conference on Electronics, Computing and Communication Technologies, IEEE Connect (July 08-10,2022) organized by IEEE Bangalore Section. Authors : vageesh maiya, janamejaya Channegowda, Chaitanya lingaraj

9. **“Synthetic data Generation using Resilient Sample Convolution and interactive Learning Approach** “at the 8th International Conference on Electronics, Computing and Communication Technologies, IEEE Connect (July 08-10,2022) organized by IEEE Bangalore Section. Authors : Vinayak Raj Urs, janamejaya Channegowda, Chaitanya lingaraj

10. **A Long Sequence Synthetic Battery Parameter Generation Perspective Using Reliable Self-Attention Mechanism** ", International Journal of Energy Research, Early Access, Q1 Journal: <https://www.scimagojr.com/journalsearch.php?q=26676&tip=sid&clean=0>

11. **Scalable Imputation Technique For Effective Computation Of Missing Battery Parameters**. Int J Energy Res. 2022;1-15. International Journal of Energy Research , Authors Joshi N, Channegowda J, Lingaraj C. <https://doi.org/10.1002/er.8548>

BOOKS

- Co-authored a Textbook “**BASIC ELECTRICAL ENGINEERING**”, ISBN: 978-81-940490-1-2

Courses Handled/List

Basic Electrical Engineering, Microcontrollers, Measurements and Instrumentation, Embedded system design, PLC and Scada.

Additional Responsibilities handled/ongoing

Coordinated for Valuation of Autonomous Exams

Coordinated VTU examination

Department Level Test coordinator

Department Level Timetable coordinator.

Coordinated in International Conference on Transformations in Engineering Education (ICTIEE)- 2015.

Faculty Event Coordinator during the National Level Tech Symposium, Phase Shift 2016,2017,2018

Faculty Coordinator – First Year inauguration and orientation programme 2017(Team Member)

Faculty co-ordinator for Graduation day(Team Member)

Farewell coordinator (Team Member)

Faculty Coordinator – Prakruthi (Nature Club of BMSCE)

Alumni Coordinator of the department

Website Coordinator

Other Information:

Execom Member of IEEE IES Bangalore chapter from 2021 till date