

**Preethi K Mane**

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### **Career Objective:**

An Energetic Engineering teacher willing to align towards the organizational objectives in providing utmost Satisfaction for a student expectation and achieving excellence in the field of Instrumentation and electronics.

### **Personal Appraisal:**

- Hard working and self-motivated with excellent communication skills.
- Punctual and able to adapt to different environments & systems.
- Ability to grasp any work quickly.
- Resourceful, enterprising and able to perform well under pressure.
- Assertive, confident and result-oriented.
- Capable of handling Team and achieve the goals

### **Educational Qualification:**

#### **Ph.D:**

Enrolled for Ph.D , year 2013 in VTU under the guidance of Dr.K.Narasimha Rao, Professor, Department of Electronics and Instrumentation Engineering for the topic “Design and Development of computational Framework for Occupancy Detection”. I have completed comprehensive and applying Machine learning to the process of Occupant activity detection.

#### **Masters in Electronics and Communication Engineering**

College :B.M.S College of Engineering  
Year of Passing : 2000

#### **Bachelor of Engineering (instrumentation Technology)**

College : Dr. Ambedkar Institute of Technology  
University : Bangalore University  
Year of Passing : 1995

## Computer Skills:

<b>Programming Languages</b>	: 8085 & 8086 Assembly Language.
<b>Operating Systems</b>	: WINDOWS 98, WIN XP and MS Dos.
<b>Word Processors</b>	: MS Word.
<b>Spread Sheets</b>	: MS EXCEL
<b>Presentation Package</b>	: MS Power Point
<b>Simulation Tools</b>	: Matlab, Lab View, Multisim, ORCAD –PCB bundle

## Strengths:

- Strong Knowledge in the theory - VLSI, Image Processing, 8086 Microprocessor
- Question papers set for examinations.
- Good in Design, Development, Implementation of procedures, Planning and monitoring skills.

## Workshops/Seminars Attended:

1. DSP - Digital DNA conducted by MOTOROLA. 18<sup>th</sup> -22 october 2000
2. Workshop on VLSI design conducted by Department of Electronics Engineering (BIT)4<sup>th</sup>- 9<sup>th</sup> September 2000
3. Database Technology,4-17<sup>th</sup> march 2002, BMSCE, Bangalore
4. Digital Controller Design and Application, 15<sup>th</sup> -28<sup>th</sup> June 2002 at BMSCE, Bangalore
5. Biotechnology for engineers, 6<sup>th</sup> -20<sup>th</sup> Jan 2003 , BMSCE, Bangalore
6. Recent Advances in Computational Techniques, 2<sup>nd</sup> – 13<sup>th</sup> Aug 2004 at BMSCE, Bangalore
7. NI Lab View Basic Training, BMSCE & NI systems pvt ltd ,SR ring road, Bangalore,29<sup>th</sup> -30<sup>th</sup> July 2010
8. Lab VIEW Machine vision Training, NI systems pvt ltd ,SR Ring road, Bangalore, 1-2<sup>nd</sup> Feb 2011
9. Matlab programming concepts, BMSCE, Bangalore,31<sup>st</sup> july to august 7<sup>th</sup> july 2012, BMSCE, Bangalore
10. Modeling of large area & Flexible Micro Electronic systems, RVCE, Bangalore
11. Virtual instrumentation for Bio Medical Signal Processing and systems, RVCE, Bangalore.
12. Antenna Technologies, B.M.S. College of Engineering, Bangalore, 27 Jan.- 1 Feb. 2014
13. Discovery Services and J-Gate, B.M.S. College of Engineering, Bangalore, 15-03-2014
14. Practical electromagnetics for modern antennas , RF and microwave designs, Entuple technologies, Bangalore, 18 -22 Aug. 2014
15. Hands-on workshop on Analog Circuits, under Texas Instruments University program, June 30-July 9 ,2014
16. Classical Control Theory and Design using MATLAB/SIMULINK , B.M.S. College of Engineering, Bangalore, 22 - 23 May 2014
17. Faculty Empowerment Programme on Formulation of Research proposals Schemes under DST/DRDO/ISRO/DIETY ,M.S. College of Engineering, Bangalore. 30-06-2015

- 18 . One week workshop on Industrial Automation, B.M.S. College of Engineering, Bangalore. 11th - 16th July 2016
19. One week workshop on Recent Advances in Industrial/Process Automation and Controls, B.M.S. College of Engineering, Bangalore. 30th-4th June 2016
20. One week workshop on Design and Simulation of Passive RF circuits , R.V. College Of Engineering, Bangalore, 20-25th June 2016
21. One week workshop on Development of Biosensors , R.V. College of Engineering, Bangalore, 4-9 July 2016
22. One week workshop on EMI and EMC in Electronic Systems , B.M.S. College of Engineering, Bangalore, 25th -29th July 2016
23. Course on Social Research Methods, B.M.S. College of Engineering, Bangalore. 9-14 October 2017
24. Two week ISTE STTP on CMOS , Mixed signal and Radio frequency VLSI Design IIT Kharagpur, 30 Jan- 4 Feb 2017
25. Awareness program on NABL Accreditation, B.M.S. College of Engineering, Bangalore, 6-7 January 2017
26. Towards safe cities: A mobile and social networking approach, GIAN course, 1st – 6<sup>th</sup> June 2018
27. Development concepts for future imaging and image guided surgeries exponential Technologies and reverse innovation , GIAN course, 8<sup>th</sup>- 14<sup>th</sup> February 2019

### **Papers presented:**

1. LPG Detection Measurement and Booking System, BM Shivalingesh, C Ramesh, SR Mahesh, R Pooja, K Preethi, S Kumuda, International Journal of Research and Scientific Innovation 1 (6), 7-10
2. Granular Mobility-Factor Analysis Framework for enriching Occupancy Sensing with Doppler Radar ,KNR, Preethi K. Mane, International Journal of Electrical and Computer Engineering (IJECE) 8 (2)
3. Computational Modelling of Bio signal-based Occupancy Sensing System using Doppler Radar, P Mane k, N Rao K, International Conference on Trends in Electronics and Informatics (ICEI 2017 )
- 4 Review of Research Progress, Trends and Gap in Occupancy Sensing for Sophisticated Sensory Operation, PK Mane, KN Rao, Computer Science On-line Conference, 212-222
- 5 An Indirect Approach for Evaluating Program Educational Objectives using Indirect Method VH ,Santosh Desai, Preethi K Mane, MITE conference 2017
- 6.TRANSLATOR FOR ‘DEAF AND DUMB’A Shikha Priyadarshni and Aprisha Roy, Preethi K Mane, S kumuda, PISER 14, Vol.02, Issue: 04/06 July-August; Bimonthly International Journal
- 7.Intelligent Automotive Telematics System with Tracking Application PKM, D AjayKumar, Madhura Hegde, International Conference on Electrical and Electronics Engineering (ICEEE 2012)

8. Face Detection System using OpenCV on Beagle Board, S Suguna Devi, PK Mane, D AjayKumar, ICEEE 2012

9. MAIL SORTER USING LABVIEW, A NIKHILA, A N NISHA, S KUMUDA, PK MANE, International Journal of Image Processing and Vision Sciences, ISSN (Print): 2278 – 1110, Volume-1, Issue-2, 2012

### Project :

#### **“Coder - Simulator Controller for Radar Applications”**

During the critical testing phase of the angle tracking circuitry of a tracking radar, in the absence of the track antenna, a simulator which simulates the functions of the track antenna is quite convenient. Keeping this in view, a coder simulator is designed. The simulator simulates the various parameters of the track antenna, which are controlled. The controller generates a menu for easy interaction and from the menu, it generates control signals for the simulator.

#### **“Micro Controller Interface implementation using FPGA”**

This project mainly deals with the interfacing of four parts with micro controller using FPGA (Field Programmable Gate array). Here four I/O ports are interfaced to microcontroller to implement the simple communication protocol. Any port which wants to communicate with microcontroller has to send a request signal to it. On receiving request signal, microcontrollers send the grant signal to the requesting port. Hence by this microcontroller establishes the data link to the requesting port. The interfacing logic required to connect the ports & microcontroller & to generate appropriate control signals, is implemented in FPGA.

#### **“Image Processing application using HDL”**

Image processing deals with the problems of obtaining measurements & manipulation of images. Image Enhancement is a technique used to accentuate certain image features for subsequent analysis or display by filtering. In order to remove the effect of noise & unwanted spurious signals filtering is necessary median filtering is applied to obtain a real time optimized response. Median filter, address generators, control unit form the functional blocks programmed in a field programmable gate array using verilog hardware descriptive language to form an application specific integrated chip.

### Recognition/Awards:

- E prayog coordinator ,2012-2015
- Best Project award – Institution-2017
- NPTEL courses – 8 weeks-2018- Elite student  
4 week-2019.
- Co-guide for team DIGI –I which participated in TI Analog design contest for the year 2010-2011

### **PCB Facility:**

Incharge for PCB prototyping facility

Aim: To inspire the students to do in house design of applications and facilitate easy prototyping.

We work on MITS PB prototyping machine to obtain the prototypes.

Steps involved

1. Design verification
2. Transfer to PCB editor
3. Create Gerber file
4. Drill, Mill and route the PCB to obtain the prototypes

### **Consultancy:**

- Generated a sum of 20,000/ Rs for an Offline 6W LED Lamp in association with ENTUPLE TECHNOLOGIES PVT LTD, Bangalore.

### **Work Experience:**

- **COLLEGE:**BMS College of Engineering  
Bull Temple Road, Bangalore-560019
- **Joining:** March 1996
- **Position:** ASSOCIATE PROFESSOR
- **DEPARTMENT:** ELECTRONICS AND INSTRUMENTATION ENGINEERING
- **DUTIES**
  - Planning of defined syllabus for the semester.
  - **Subject Handled :**
    - VLSI Design
    - Bio – Medical Instrumentation
    - Communication System
    - Analog Electronic Circuits
    - Linear integrated circuits
    - Analytical Instrumentation
    - Electronic Measurements
    - Digital image processing
    - Artificial neural networks and Fuzzy logic
  - Communicating concepts and creating interest in the theory and practical sessions
  - Interact with the students with respect to sessions conducted, requirement, acceptance and updates regarding the subject
  - Served as Feedback co ordinator
  - Departmental Procurement Committee
  - Project Work Guidance for Engineering Students, proctoring.

- Support activities in developing literature for Departmental activities like modrobs fund, accreditation, NAAC.
- Board Of Examiner(Department)

### **Personal Details:**

Permanent Address : # 22/5 East Anjaneya Temple Street,  
Basavanagudi,  
Bangalore – 560 004.

Residential Address : NO 207,Sky Line Surabhi Apartment,  
No 14,Vidhyapeetha Road,  
Banashankari III stage  
Bangalore-85

Fathers Name : A. Krishna Rao Mane

Date of Birth : 31<sup>st</sup> May 1974